

High-Side and Low-Side Gate Driver



■ Floating high-side driver in bootstrap operation to 200V

TF Semiconductor

- Drives two N-channel MOSFETs or IGBTs in high-side/ low-side configuration
- Outputs tolerant to negative transients
- Wide low-side gate driver and logic supply: 10V to 20V
- Logic inputs CMOS and TTL compatible (down to 3.3V)
- Schmitt triggered logic inputs with internal pull down
- Delay matching of 30ns maximum

tfss

- Source/sink pulsed current of 290mA/600mA typical
- Undervoltage lockout for high-side and low-side drivers
- Space-saving SOIC-8 package available
- Extended temperature range:-40°C to +125°C

Description

The TF2005M is a mid voltage, high speed gate driver capable of driving N-channel MOSFETs and IGBTs in a high-side/low-side configuration. TF Semiconductor's high voltage process enables the TF2005M's high-side to switch to 200V in a bootstrap operation. The 30ns (max) propagation delay matching between the high and the low side drivers allows high frequency switching.

The TF2005M logic inputs are compatible with standard TTL and CMOS levels (down to 3.3V) for easy interfacing with controlling devices. The driver outputs feature high pulse current buffers designed for minimum driver cross conduction. The low-side gate driver and logic share a common ground

The TF2005M is available in a space-saving 8-pin SOIC package and the operating temperature extends from -40° C to $+125^{\circ}$ C.

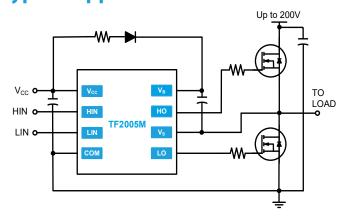


SOIC-8(N)

Applications

- DC-DC Converters
- AC-DC Inverters
- Motor Controls
- Class D Power Amplifiers

Typical Application



Ordering Information

Year Year Week Week

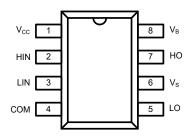
PART NUMBER	PACKAGE	PACK / Qty	MARK
TF2005M-TAU	SOIC-8(N)	Tube / 100	YYWW TFTF2005M Lot ID
TF2005M-TAH	SOIC-8(N)	T & R/ 2500	YYWW TF>TF2005M Lot ID

www.tfsemi.com Rev. 1.0

Dec. 2022







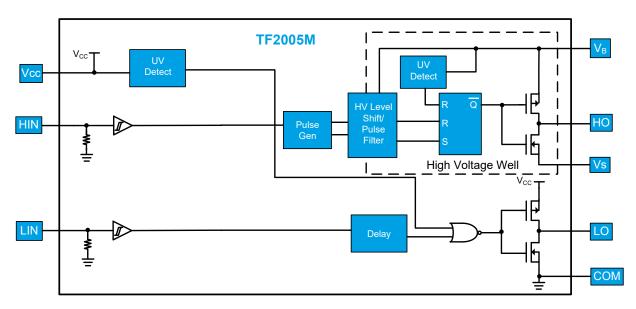
Top View: SOIC-8

TF2005M

Pin Descriptions

PIN NAME	PIN DESCRIPTION	
HIN	Logic input for high-side gate driver output (HO), in phase	
LIN	Logic input for low-side gate driver output (LO), in phase	
V _B	High-side floating supply	
НО	High-side gate drive output	
V _s	High-side floating supply return	
V _{cc}	Low-side and logic fixed supply	
LO	Low-side gate drive output	
COM	Low-side return	
NC	"No connect" pin	

Functional Block Diagram



Dec. 2022

3

High Side and Low Side Gate Driver

Rev.	Change	Owner	Date
1.0	First release, AI datasheet	D. Walton	12/01/2022

for additional information contact: info@tfsemiconductors.com

Important Notice

TF Semiconductor Solutions (TFSS) PRODUCTS ARE NEITHER DESIGNED NOR INTENDED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS UNLESS THE SPECIFIC TFSS PRODUCTS ARE SPECIFICALLY DESIGNATED BY TFSS FOR SUCH USE. BUYERS ACKNOWLEDGE AND AGREE THAT ANY SUCH USE OF TFSS PRODUCTS WHICH TFSS HAS NOT DESIGNATED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS IS SOLELY AT THE BUYER'S RISK.

TFSS assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using TFSS products.

Resale of TFSS products or services with statements different from or beyond the parameters stated by TFSS for that product or service voids all express and any implied warranties for the associated TFSS product or service. TFSS is not responsible or liable for any such statements.

©2022 TFSS. All Rights Reserved. Information and data in this document are owned by TFSS wholly and may not be edited, reproduced, or redistributed in any way without the express written consent from TFSS.

For additional information please contact support@tfsemi.com or visit www.tfsemi.com

Dec. 2022